

Ali Vahit Şahiner, birth 1956 Ankara

Areas of Interest

Computer Graphics, Visualization and Animation
Computer Technologies in TV and Video Production
Graphic Design and Typography
Multimedia and Internet Technologies
Parallel and Distributed Processing

Academic Background

- DEC. 1991** Ph.D., Dept. of Computer Science, Univ. of Westminster, London
Thesis: A Computational Model for Parallelism: Self-Adapting Parallel Servers
- FEB. 1982** M.Sc., Dept. of Computer Engineering, METU, Ankara
Thesis: A Character Recognition System for Turkish Text Reading
- FEB. 1979** B.Sc., Dept. of Management, METU, Ankara

Professional Background

- 2004 -** Instructor, Boğaziçi University, İstanbul
Courses given:
Computer Graphics
Object Oriented Programming
- 2000 - 2002** Assist.Prof. Yeditepe University, Dept. Of Information and Computer Science, İstanbul
- 1995 -** Director, Story Mine, Istanbul
Digital Design Studio: Multimedia and Internet applications, Visualisation, 2D and 3D Animation
- 1991 - 1995** Research Officer, Queen Mary & Westfield College, University of London
Worked on the MonaLisa Project, a collaborative RACEII project undertaken by nine European partners which include BBC, Siemens, and Thomson CSF. Developed the system software for the multiprocessor accelerator. The project demonstrator, a real-time virtual studio system for TV/video production and live broadcasting was shown at 1996 IBC in Amsterdam and later marketed by Accom.
- 1988 - 1989** Software Engineer, Zebra Parallel Ltd., London
Worked on the development of Equus, an environment for reconfigurable distributed computations that execute at processing nodes which are dynamically allocated from a pool. Developed image processing and object recognition applications running under Equus.

1985 - 1988 Research Assistant, Computer Science Dept., University of Westminster, London

Research on distributed systems, distributed and parallel image processing. A prototype version of Equus named WORMOS was developed.

1982 - 1985 Research Assistant, Computer Science Dept., METU, Ankara

Research on Pattern and Character Recognition.

Publications

Diktas E.D., Sahiner A.V., *Minkowski Sum Based Octree Generation for Triangular Meshes*, A. Levi et al. (Eds.), ISCS 2006, Lecture Notes in Computer Science 4263, Springer-Verlag, 2006, pp. 363–373.

Altılar T.D., Paker Y., Sahiner A.V., *A Parallel Architecture for Video Processing*, High-Performance Computing and Networking, Lecture Notes in Computer Science 1225, Eds. Herzberger B., et al., Springer-Verlag, 1997, pp. 929.

Kindberg T., Sahiner A.V., Paker Y., *Adaptive Parallelism under Equus*, Proceedings of Second International Workshop on Configurable Distributed Systems, IEEE, March 1994, pp.172.

Sahiner A.V., Lefloch P., Paker Y., *A Parallel Accelerator for Using Synthetic Images in TV and Video Production*, (Eds.) Paker Y., et al., Proceedings of the European Workshop on Combined Real & Synthetic Image Processing for Broadcast and Video Production, Springer-Verlag, London, 1994, pp.13.

Lefloch P., Sahiner A.V., Paker Y., *Visual Tools for Parallel Server Handling*, (Eds.) Paker Y., et al., Proceedings of the European Workshop on Combined Real & Synthetic Image Processing for Broadcast and Video Production, Springer-Verlag, London, 1994, pp.35.

Routsis D., Lefloch P., Sahiner A.V., Thomas G., *Real-Time Camera Tracking on the ELSET Accelerator*, (Eds.) Paker Y., et al., Proceedings of the European Workshop on Combined Real & Synthetic Image Processing for Broadcast and Video Production, Springer-Verlag, London, 1994, pp.84.

Sahiner A.V., Lefloch P., Nimmo A., Paker Y., *A Parallel Accelerator for Generating Virtual Studio Sets*, Eight Eurographics Workshop on Graphics Hardware, Eds. Lister P.F., Eurographics Tech. Report Series ISSN 1017-4656, 1993, pp.62.

Sahiner A.V., *Shape Recognition on a Parallel Architecture*, PARBASE-90 Conf. On Databases Parallel Architectures and Their Applications 1990 Proceedings, (Eds.) N. Rische, et al., pp.475.

Sherman A., Sahiner A.V., Kindberg T., *Horsebox*, Parallelogram, No.14, May 1989, No.14, pp.18.

Sahiner A.V., *A Distributed Architecture for Image Processing*, Parallel Architectures and Computer Vision, (Eds.) Page, I., Oxford University Press, Oxford, 1988, p.187.

Kindberg T., Sahiner A.V., Paker Y., *Worm Programs*, Distributed Operating Systems: Theory and Practice, NATO ASI Series, (Eds.) Paker Y., et al., Springer-Verlag, Heidelberg, 1987, pp. 355.